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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Brian G. Johnson et al.

Serial No.: 10/634,130

Filed: August 4, 2003

For: Multilayered Phase  
Change Memory

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Art Unit: 2818

Examiner: Thao P. Le

Docket: ITO.0045US  
P16093

Assignee: Intel Corporation

Mail Stop **Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO OFFICE ACTION**

Sir:

In response to the office action mailed October 16, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: December 6, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Cynthia L. Hayden